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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/724,117	12/01/2003	Kazuo Suto	SEC.468C	9920
20,0,	7590 01/31/200 FRANCOS, & WHITT	EXAMINER		
ONE FREEDOM SQUARE 11951 FREEDOM DRIVE SUITE 1260 RESTON, VA 20190			CHOE, HENRY	
			ART UNIT	PAPER NUMBER
			2817	
SHORTENED STATUTOR	Y PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE	
3 MONTHS		01/31/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

	Application No.	Applicant(s)					
	10/724,117	SUTO, KAZUO					
Office Action Summary	Examiner	Art Unit					
	Henry K. Choe	2817					
The MAILING DATE of this communication app							
Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).							
Status							
1)⊠ Responsive to communication(s) filed on <u>05 Ja</u>	nuary 2007.						
•	action is non-final.						
,							
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims							
4)⊠ Claim(s) <u>20-25</u> is/are pending in the application.							
4a) Of the above claim(s) is/are withdrawn from consideration.							
5) Claim(s) is/are allowed.	,						
6)⊠ Claim(s) <u>20 and 24</u> is/are rejected.							
7)⊠ Claim(s) <u>21-23 and 25</u> is/are objected to.							
Application Papers							
9) The specification is objected to by the Examiner.  10) ☑ The drawing(s) filed on <u>01 December 2003</u> is/are: a) ☑ accepted or b) ☐ objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).							
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority under 35 U.S.C. § 119							
12)⊠ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a)⊠ All b)☐ Some * c)☐ None of:							
1.☐ Certified copies of the priority documents have been received.							
2. Certified copies of the priority documents have been received in Application No. 10/053,897.							
3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).							
* See the attached detailed Office action for a list of the certified copies not received.							
Attachment(s)  1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)							
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) Paper No(s)/Mail Date.							
3) Information Disclosure Statement(s) (PTO/SB/08)  Paper No(s)/Mail Date 12/1/03.  5) Notice of Informal Patent Application  6) Other:							
Paper No(s)/Mail Date <u>12/1/03</u> .	6) [						

## **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 20 and 24 are rejected under 35 U.S.C. 102(b) as being anticipated by Rice et al (Figs. 4 and 6).

Regarding claim 20, Rice et al (Figs. 4 and 6) discloses an oscillator circuit comprising a PLL circuit (35, 45), an AGC circuit (65) which controls a gain of a modulating signal (70) and outputs a control signal (output of 65), a voltage controlled oscillation circuit (55) which controls an oscillation frequency of a signal (50) outputted from the PLL circuit (35, 45) based on the control signal (output of 65) and wherein the voltage controlled oscillation circuit (55) includes a first voltage controlled reactance unit (220 in Fig. 6) which inputs the signal (50) outputted from the PLL circuit (35, 45), a second voltage controlled reactance unit (215 in Fig. 6) which inputs the control signal (output of 65), and a high-frequency oscillation circuit (235, 240) which connects in parallel with the first (220 in Fig. 6) and second (215 in Fig. 6) voltage controlled reactance units which outputs the input signal.

Regarding claim 24, the first voltage controlled reactance unit (220) includes a first varactor diode and a first capacitor and wherein the cathode of the first varactor diode (D1) is connected to one end of the first capacitor (C1), and the signal (50)

Application/Control Number: 10/724,117 Page 3

Art Unit: 2817

outputted from the PLL circuit (35, 45) is inputted where the cathode and the one end are connected each other.

## Allowable Subject Matter

Claims 21-23 and 25 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

## Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent numbers (6,476,684; 4,994,768) are the PLL circuits with the control circuits.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Henry Choe whose telephone number is (571) 272-1760.

HENRY CHOE
PRIMARY EXAMINER